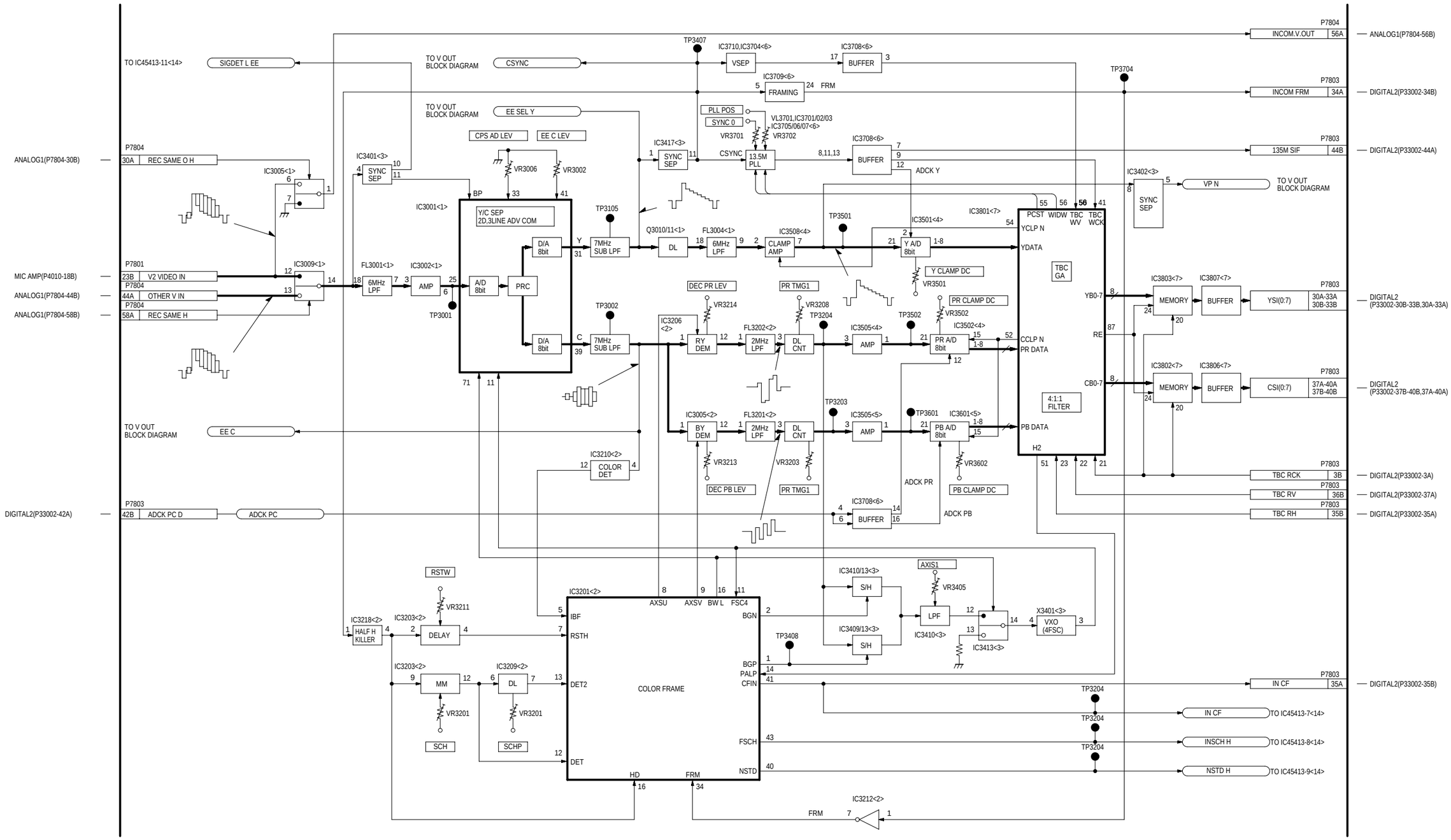
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MIC AMP(P4010-18B)
ANALOG1(P7804-44B)
ANALOG1(P7804-58B)
DIGITAL2(P33002-42A)
P7804
30A REC SAME O H
P7801
23B V2 VIDEO IN
P7804
44A OTHER V IN
P7804
58A REC SAME H
P7804
42B ADCK PC D
ADCK PC
TO IC45413-11<14>
SIGDET L EE
TO V OUT BLOCK DIAGRAM
CSYNC
EE SEL Y
EE C
CPS AD LEV
EE C LEV
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VR3002
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IC3401<3>
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IC3001<1>
Y/C SEP 2D,3LINE ADV COM
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A/D 8bit
PRC
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TP3001
TP3105
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Y CLAMP DC
VR3501
IC3801<7>
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YDATA
TBC GA
TP3002
DEC PR LEV
PR TMG1
VR3214
VR3208
PR CLAMP DC
TP3502
VR3502
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CCLP N
PR DATA
IC3206 <2>
RY DEM
FL3202<2>
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DL CNT
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IC3505<4>
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4:1:1 FILTER
IC3005<2>
BY DEM
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PB DATA
H2
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VR3213
DEC PB LEV
VR3203
PR TMG1
ADCK PR
VR3602
PB CLAMP DC
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ADCK PB
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SYNC 0
VL3701,IC3701/02/03
IC3705/06/07<6>
VR3701
VR3702
IC3417<3>
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13.5M PLL
8,11,13
BUFFER
ADCK Y
IC3402<3>
SYNC SEP
VP N
TO V OUT BLOCK DIAGRAM
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INCOM.V.OUT 56A
ANALOG1(P7804-56B)
P7803
INCOM FRM 34A
DIGITAL2(P33002-34B)
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135M SIF 44B
DIGITAL2(P33002-44A)
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IC3807<7>
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MEMORY
IC3806<7>
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CSI(0:7) 37A-40A 37B-40B
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RE
CB0-7
P7803
TBC RCK 3B
DIGITAL2(P33002-3A)
TBC RV 36B
DIGITAL2(P33002-37A)
TBC RH 35B
DIGITAL2(P33002-35A)
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HALF H KILLER
IC3203<2>
DELAY
IC3201<2>
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IBF
RSTH
DET2
DET
HD FRM
COLOR FRAME
BGN
BGP
PALP
CFIN
FSCH
NSTD
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S/H
IC3409/13<3>
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LPF
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IC3413<3>
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VXO (4FSC)
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SCH
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VR3201
SCHP
IC3212<2>
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TO IC45413-9<14>